

FIG.1A

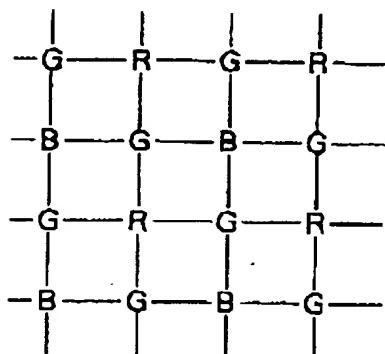


FIG.1B

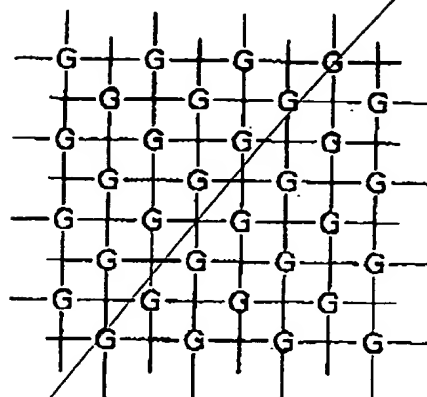


FIG.1C

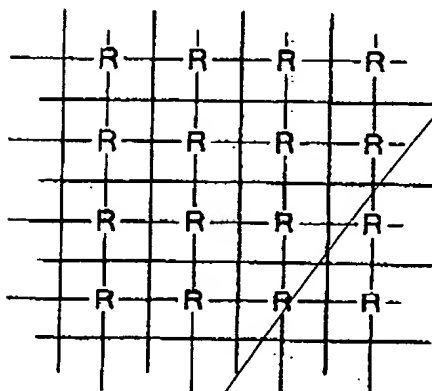


FIG.1D

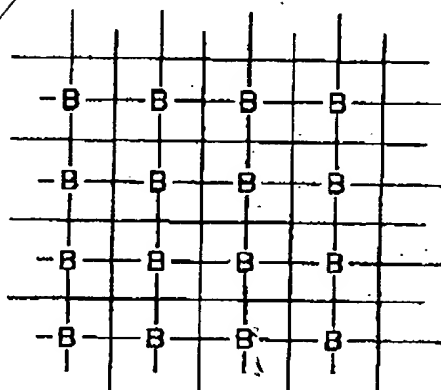


FIG.1E

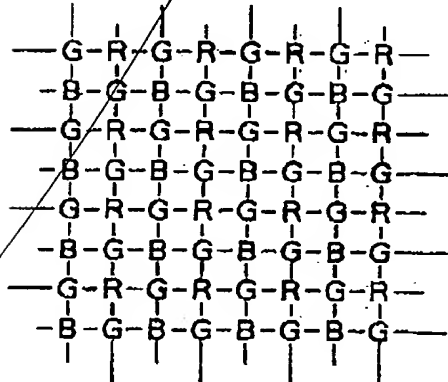


FIG.2

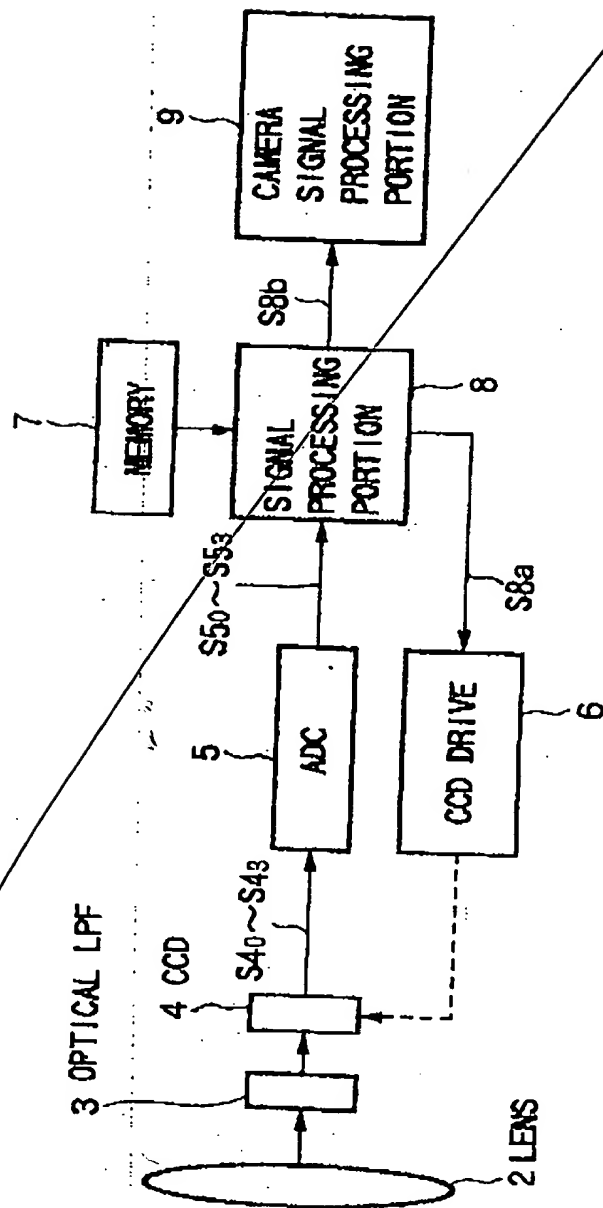


FIG.3

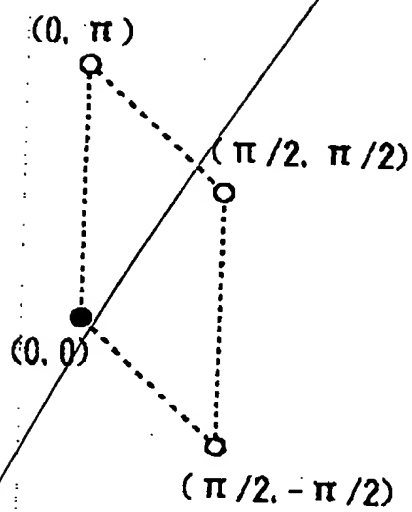


FIG.4A

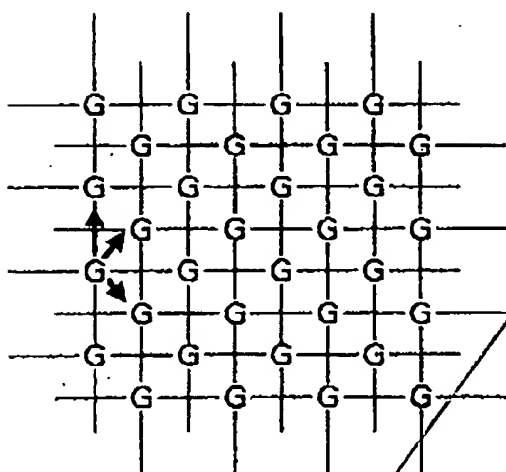


FIG.4B

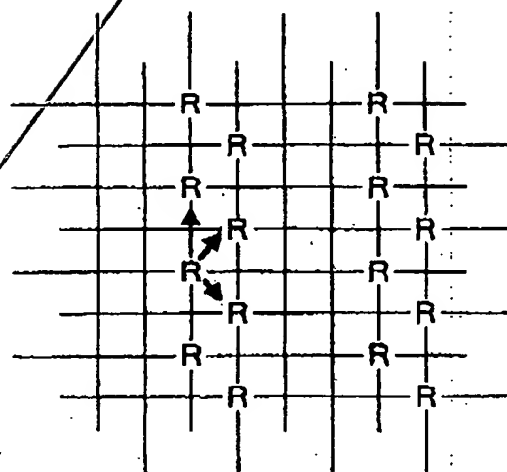


FIG.4C

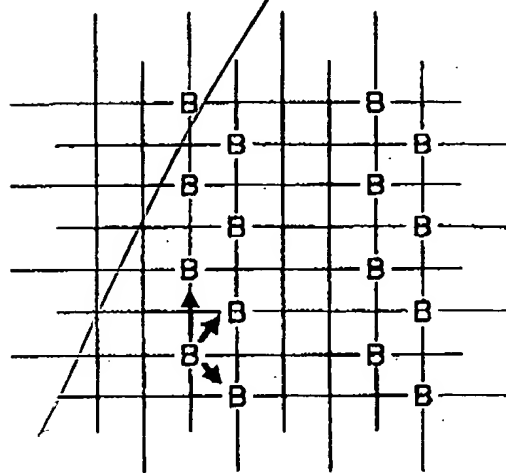


FIG.5A

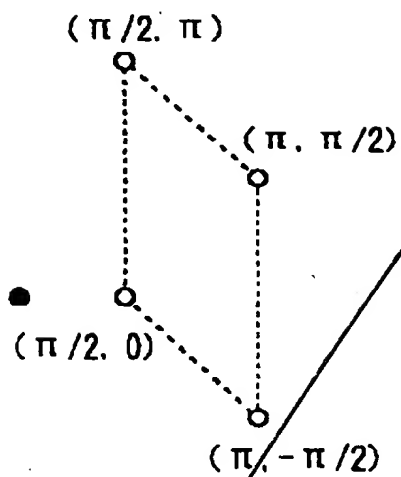


FIG.5B

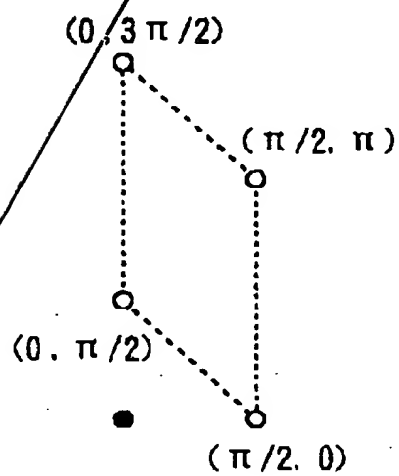
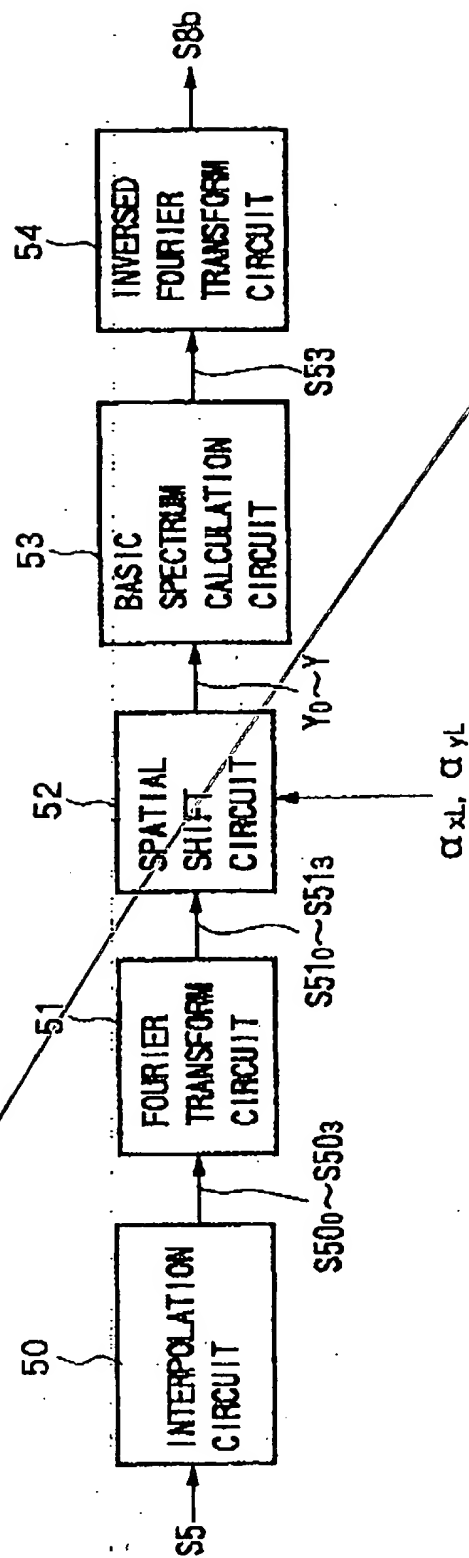


FIG.6



8 SIGNAL PROCESSING PORTION

FIG.7

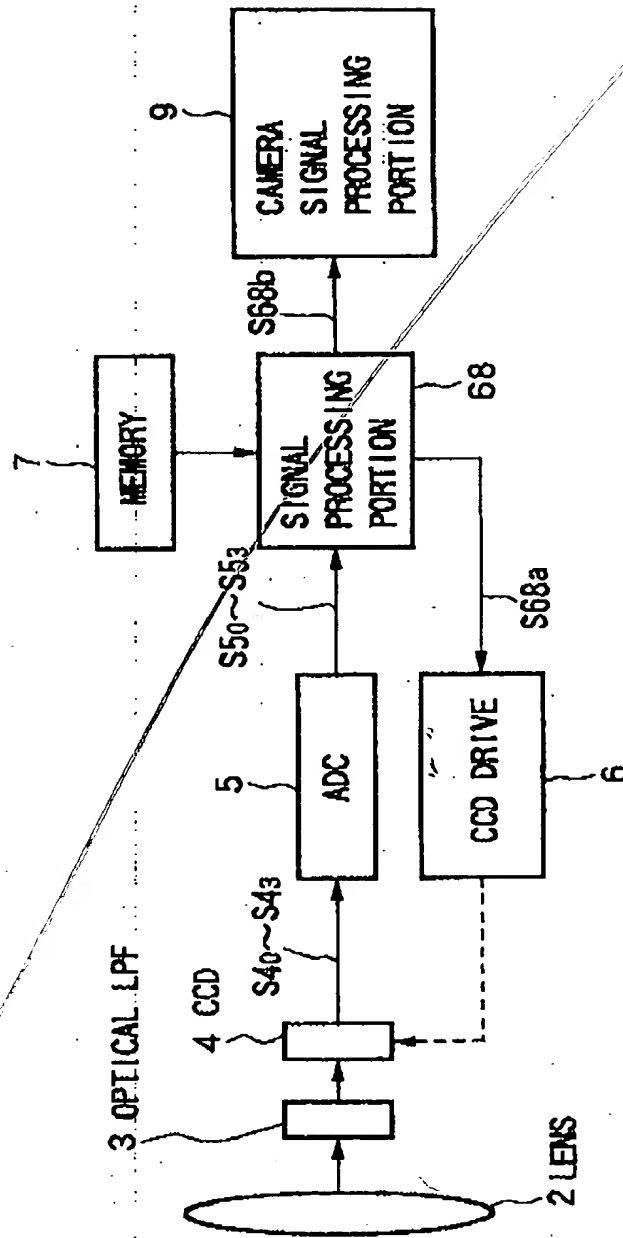


FIG.8

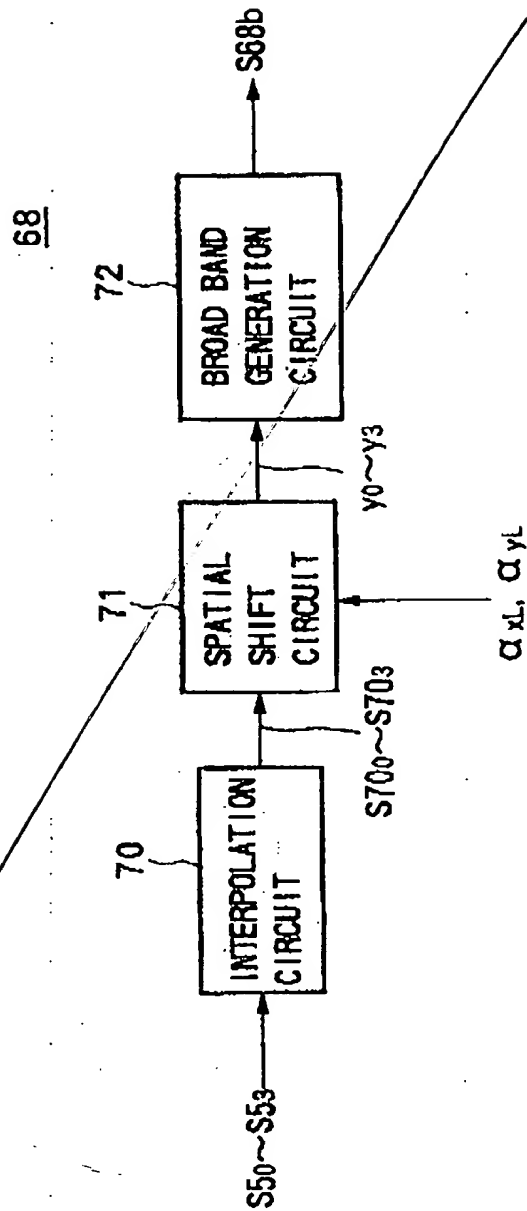




FIG.9

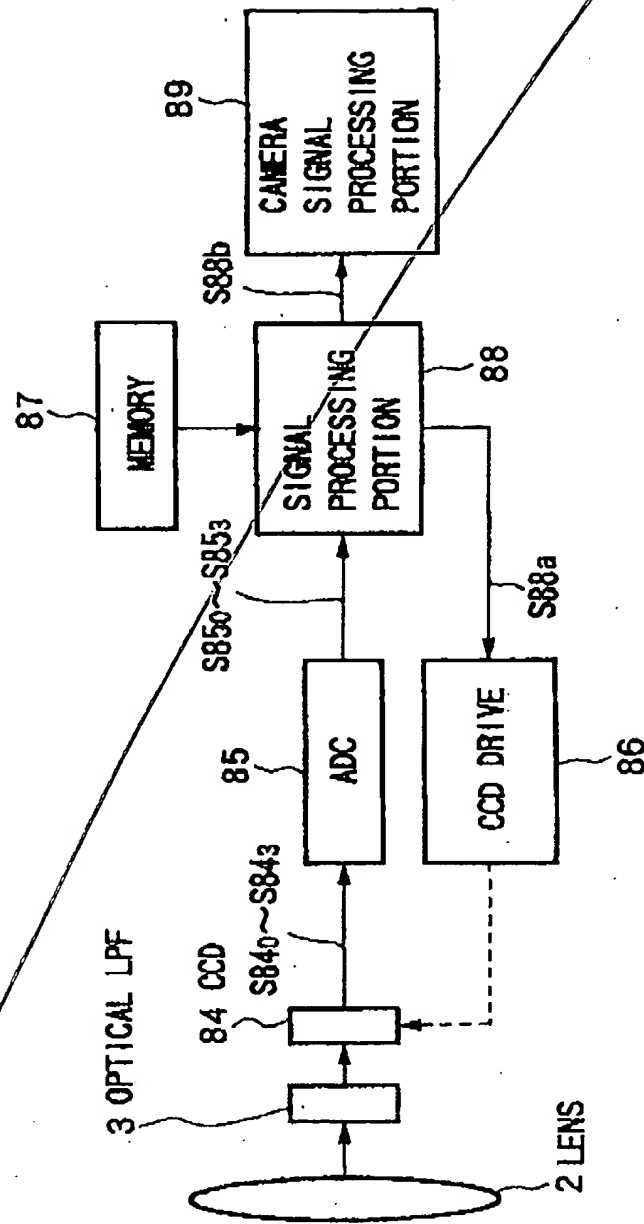


FIG.10A

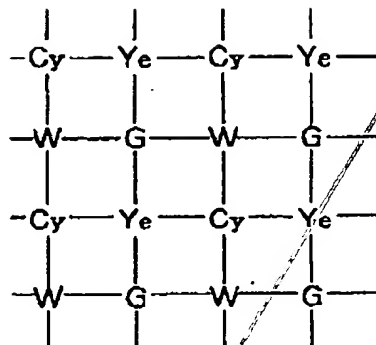


FIG.10A

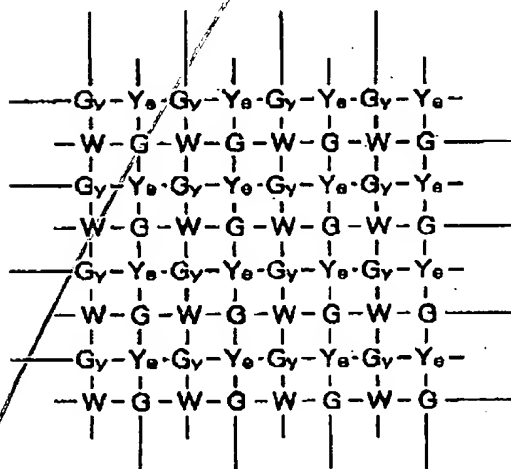


FIG.10C

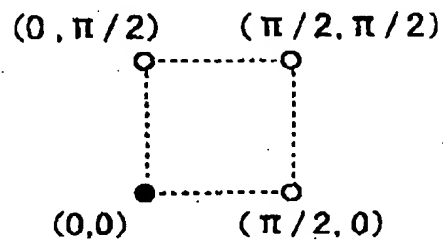


FIG.11

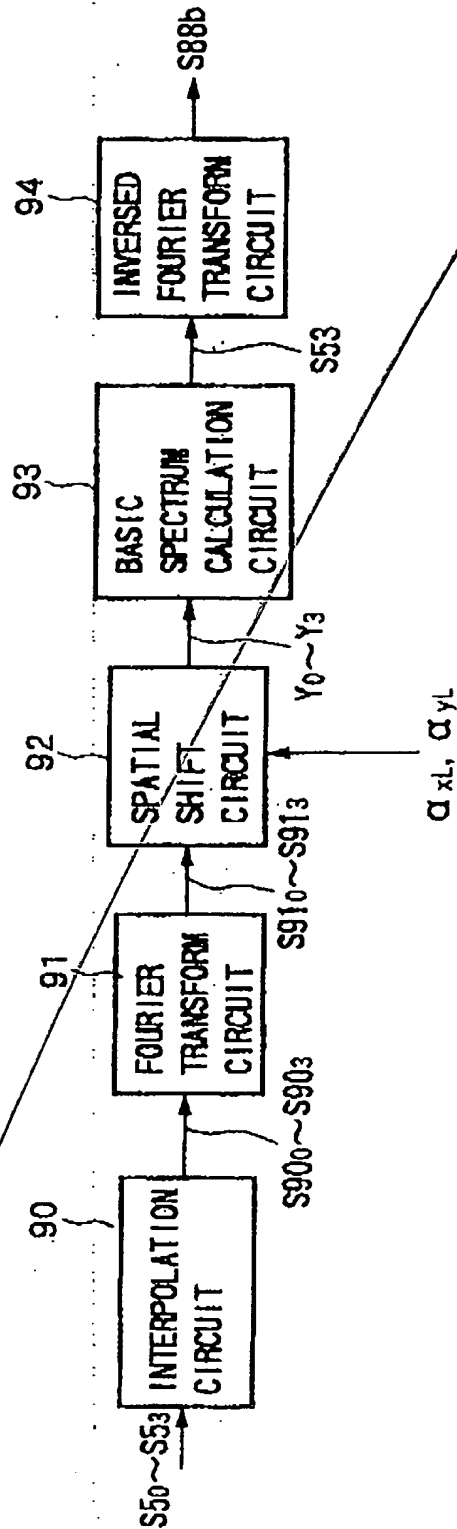


FIG.12A

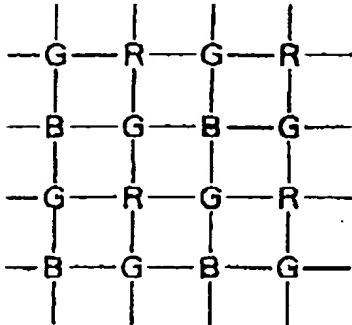


FIG.12B

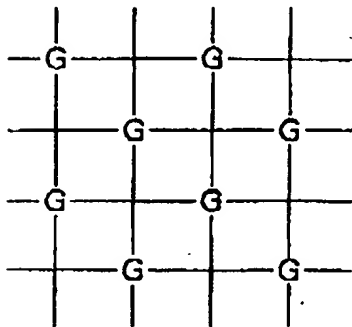


FIG.12C

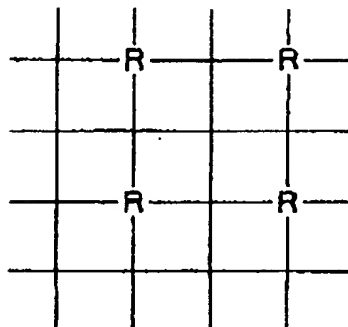


FIG.12D

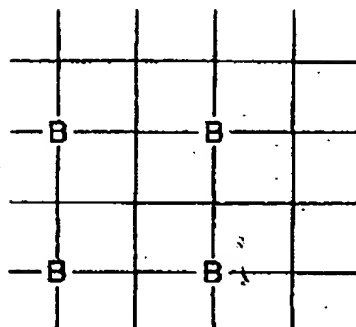


FIG.12E

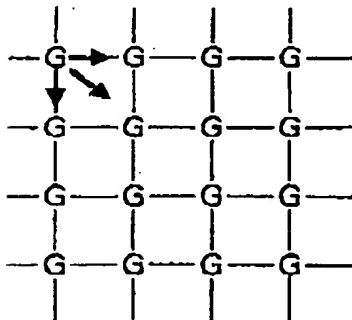


FIG.12F

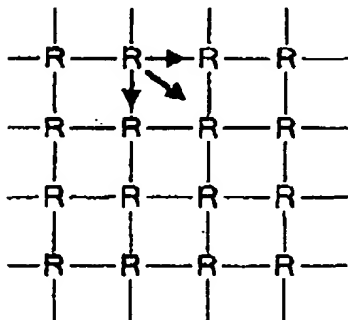


FIG.12G

FIG.12F

